

11117 U.S. PTO  
09/805137



Class	716
Subclass	1
ISSUE CLASSIFICATION	

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PATENT NUMBER

U.S. UTILITY Patent Application

SCANNED <i>043</i>	O.I.P.E. <i>Ann</i>	PATENT DATE
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APPLICATION NO. 09/805137	CONT/PRIOR D	CLASS <del>248</del> 716	SUBCLASS <del>320</del> 1	ART UNIT 218T 2825	EXAMINER <i>Thompson</i> <i>David A. Thompson</i>
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APPLICANTS  
William Moore  
Sebastian Ventrone

TITLE  
Microprocessor including controller for reduced power consumption and method therefor

PTO-2040  
1299

ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)					
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)				
716	1	716	2	4			
INTERNATIONAL CLASSIFICATION							

☒ Continued on Issue Slip inside File Jacket

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b> <input checked="" type="checkbox"/> The term of this patent subsequent to (date) has been disclaimed. <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent No. _____ <input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg. 5	Figs. Drwg. 9	Print Fig.	Total Claims 6	Print Claim for O.G. 1
(Assistant Examiner) _____ (Date) _____ <i>A. M. Thompson</i> 11-2003 (Primary Examiner) (Date)			<b>NOTICE OF ALLOWANCE MAILED</b> _____ <b>ISSUE FEE</b> Amount Due _____ Date Paid _____		
(Legal Instruments Examiner) _____ (Date) _____ <b>ISSUE BATCH NUMBER</b> _____					

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